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UNITED STATES PATENT APPLICATION

of

**Martin Dauelsberg**  
Kuckhoffstrasse 4  
D-52064 Aachen, Germany

for

**Method For Depositing In Particular Crystalline Layers**

Attorney for Applicant

Wesley W. Whitmyer, Jr., Registration No. 33,558  
**ST.ONGE STEWARD JOHNSTON & REENS LLC**  
986 Bedford Street  
Stamford, CT 06905-5619  
203 324-6155

METHOD FOR DEPOSITING IN PARTICULAR CRYSTALLINE LAYERS

[0001] This application is a continuation of pending International Patent Application No. PCT/EP01/14064 filed December 1, 2001, which designates the United States and claims priority of pending German Application No. 100 64 942 filed December 23, 2000.

Field Of The Invention

[0002] The invention relates to a method for depositing in particular crystalline layers on in particular crystalline substrates in a process chamber of a reactor housing with a water-cooled wall, the base of the process chamber being heated, and one or more reaction gases being introduced centrally into the process chamber as process gas, together with hydrogen as carrier gas, and being discharged through a gas outlet ring which surrounds the process chamber, a purge gas flowing between reactor cover and processing chamber ceiling, which purge gas, together with a purge gas which purges the space between reactor wall and gas outlet ring, is passed through a gap between reactor cover and the gas outlet ring, which can be lowered to allow loading of the process chamber, into the outer region of the process chamber, in order to escape into the gas outlet ring together with the process gas through openings.

Background Of The Invention

[0003] US patent 4,976,217 describes a method and device for depositing III-V semiconductor layers in a reactor which is of cylinder-symmetrical form. The process chamber has a base which is heated by high frequency from below. The process chamber is surrounded by an annular hollow body which has radially inwardly directed openings through which the process gas, which is introduced centrally into the process chamber, is discharged. The rear, radially outer space around this gas outlet ring is

surrounded by a cold reactor wall. The gap between the reactor wall and the gas outlet ring is purged by a purge gas.

[0004] US patent 5,027,746 likewise describes a device for the epitaxial growth of II-V semiconductor layers. In this case too, the process gases are fed centrally to a process chamber, the base of which is heated. The process gases are discharged through a gas outlet ring which surrounds the process chamber. In this device too, gaps between the individual components of the process chamber are purged by means of purge gases. In this case, in particular, the space between the reactor cover and the process chamber ceiling is purged by a mixture of hydrogen and argon. The thermal conductivity of the purge gas, and therefore the cooling of the process chamber ceiling, can be influenced by varying the gas composition.

[0005] The device of the generic type has a gas outlet ring which is lowered in order for the process chamber to be loaded, for which purpose the reactor cover and the process chamber ceiling secured to it are raised. In the known device, in which the method of the generic type is carried out, undesirable particles may be released when the gas outlet ring is lowered. The particles are released in particular from the gas outlet openings of the gas outlet ring and originate from parasitic occupancy of the interior of the gas outlet ring.

[0006] The invention is based on the object of carrying out the method in such a way that this formation of particles is reduced.

[0007] The object is achieved by the invention described in the claims.

[0008] Claim 1 proposes that the gas which purges the space between the reactor wall and the gas outlet ring be nitrogen or a mixture of hydrogen and nitrogen. The solution according to the invention is based on the discovery that the parasitic growth inside the hollow gas outlet ring and in the region of

the gap between gas outlet ring and that edge section of the process chamber ceiling which engages over the gas outlet ring in regions occurs as a result of a wall temperature which is too low and/or as a result of diffusion of a process gas into the gap. The purge gas selected not only reduces its thermal conductivity; rather, the measure proposed by Claim 1 is also associated with a change in the diffusion characteristics within the purge gas. On account of the reduced thermal conductivity, less heat is released by the radially outwardly facing wall of the gas outlet ring to the cooled reactor wall. The result of this is that the rear wall of the hollow interior of the gas outlet ring remains at a higher temperature than with the known procedure. Furthermore, the reduced diffusion counteracts the penetration of process gas into the gap between process chamber ceiling and gas outlet ring. The procedure according to the invention is advantageous in particular in processes in which substrates with a relatively large diameter, for example seven substrates each with a diameter of six inches are located in planetary form in the process chamber. The process chamber than has a relatively large diameter, making the gap between gas outlet ring and the water-cooled boundary wall of a standard reactor very small. The procedure according to the invention then leads to a correspondingly increased temperature gradient being present in the gap. Furthermore, it is beneficial if, in a known way, the gap space between the process chamber ceiling and the reactor cover is likewise purged with nitrogen or a mixture of hydrogen and nitrogen. This purge gas stream then passes through a gap between the edge of the process chamber ceiling and a spacer ring onto the outer wall, facing the process chamber ceiling, of the gas outlet ring. At this location, the gas which purges the cover gap is mixed with the gas which purges the boundary gap, with the result that the gas which flows into the periphery of the process chamber has an increased volumetric flow. The purge gas which flows into the periphery of the process chamber is sucked out together with the process gas through the gas exit openings of the gas outlet ring.

[0009] An exemplary embodiment of the invention is explained in more detail below with reference to appended drawings.

[00010] The drawing represents a highly diagrammatic illustration of the periphery of a cylinder-symmetrical process chamber, in cross section.

[00011] The process chamber 1 extends horizontally above the process chamber base 3, which is heated from below by means of an HF heating means 16 and consists of graphite. In the process chamber base 3 there is a multiplicity of, in particular seven, cylindrical cutouts, in each of which a disk-like substrate holder 21 is mounted rotatably on a gas cushion 22. The process gas 10 flows through the process chamber 1, radially outward from its center. The process chamber 1 is delimited at the top by the process chamber ceiling 4, which may consist of quartz or may likewise consist of graphite. Between the process chamber ceiling 4 and the reactor cover 6, which consists of stainless steel, there is a gap 13 which is purged from the center outward by means of a purge gas 7.

[00012] The peripheral boundary of the process chamber 1 is formed by a gas outlet ring 5, which is formed as a hollow body. That wall 5" of the gas outlet ring 5 which faces toward the process chamber 1 has a multiplicity of openings 11 through which the process gas 10, together with the purge gas flowing into the periphery of the process chamber 1, is sucked out. The gas which has been sucked out emerges from the hollow interior of the gas outlet ring 5 through one or more gas exit openings 19 which are associated with the base-side wall 5"". A pump may be connected to this opening 19.

[00013] To the rear of the gas outlet ring 5, between the rear wall 5' and the reactor wall 2, there is a narrow space 12. This space is purged by a purge gas 8. The purging takes place from the bottom upward, so that the purge gas 8 is diverted at a spacer ring 18, which is disposed in the corner region of the

reactor and may consist of Ceran, in order to flow over the upper wall 5" of the gas outlet ring 5.

[00014] Between the process chamber ceiling and the spacer ring 18 there is an annular gap 17, through which the purge gas 7 can escape, in order to mix with the purge gas 8 for the space 12 and to flow through the gap 9 between the upper wall 5" and the edge section of the process chamber ceiling into the outer section 1' of the process chamber 1, where it is sucked out through the opening 11 together with a further purge gas stream 15 and the process gas 10. The purge gas 15 purges the gap 14 which is located between the peripheral edge of the process chamber base 3 and the inwardly facing wall 5" of the gas outlet ring 5.

[00015] According to the invention, it is proposed for the space 12, which is only a few millimeters wide, between the wall 5' and the reactor wall 12, which includes purge water passages 20, to be purged by means of nitrogen or a mixture of hydrogen and nitrogen. This mixture has a significantly lower thermal conductivity than pure hydrogen, with the result that the wall 5' of the gas outlet ring 5, in particular on its inner side, remains at a temperature which is higher than the deposition temperature of the process gases, which may be phosphine, arsine and metalorganic compounds. The result of this is that no deposition or only a small amount of deposition occurs in the hollow interior of the gas outlet ring. If the gas outlet ring 5 is lowered in order for the process chamber 1 to be loaded with substrates, therefore, it is impossible for any particles to escape outward from the interior of the gas outlet ring 5 through the openings 11.

[00016] Moreover, the fact that the hydrogen/nitrogen gas stream also purges the gap 9 between reactor ceiling 4 and gas outlet ring 5 effectively prevents process gas from diffusing into the gap 9, where it could condense at cold locations. If the gas stream 7, which likewise consists of a mixture of

hydrogen and nitrogen, is admixed with this gas stream, the diffusion properties are reduced still further.

[00017] All features disclosed are (inherently) pertinent to the invention. The disclosure content of the associated/appended priority documents (copy of the prior application) is hereby incorporated in its entirety in the disclosure of the application, partly with a view to incorporating features of these documents in claims of the present application.